

JCWSCS 0 8 SEP 2004

NO. 713 P. 1/5

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
BORGATTI ET AL.) Attorney Docket No. 53426
)
Serial No. 10/768,401)
Confirmation No. 3793)
Filing Date: JANUARY 30, 2004)
)
For: RECONFIGURABLE SIGNAL)
PROCESSING IC WITH AN EMBEDDED)
FLASH MEMORY DEVICE)
)

REQUEST FOR CORRECTION TO THE FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The filing receipt for the subject patent application has been received and reviewed for accuracy. Several discrepancies have been discovered. The filing receipt in error lists the address of the first listed inventor as "Finale Emilia, ITALY". The correct address is Finale Emilia (MO), ITALY. The next error lists the address of the second inventor as "Monza, ITALY". The correct address is Monza (MI), ITALY. The third error lists the address of the third inventor as "San Colombano Certenoli, ITALY". The correct address is San Colombano Certenoli (GE), ITALY. The fourth error lists the address of the fourth inventor as "San Martino Siccomario, ITALY". The correct address is San Martino Siccomario (PV), ITALY. The fifth error lists the address of the fifth inventor as "Monleale, ITALY". The correct address is Monleale (AL), ITALY. The sixth error list the address of the Assignee as "Agrate Brianza, ITALY". The correct address is Agrate Brianza (MI), ITALY.

In re Patent Application of:
BORGATTI ET AL.
Serial No. 10/768,401
Filing Date: JANUARY 30, 2004

In view of the errors noted, it is respectfully requested that a corrected filing receipt be issued in this application. If any fee is believed to be necessary, authorization is given to charge Deposit Account No. 01-0484.

Respectfully submitted,



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CERTIFICATE OF FACSIMILE

I HEREBY CERTIFY that this REQUEST FOR CORRECTION TO THE FILING RECEIPT for U.S. Serial No. 10/768,401 is being submitted via telefacsimile no. 703-746-9195 to: OFFICE OF INITIAL PATENT EXAMINATION'S FILING RECEIPT CORRECTIONS, P.O. BOX 1450 ALEXANDRIA, VA 22313-1450, this 8th day of September, 2004.





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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/768,401	01/30/2004	2183	1026	02AG38953426	6	27	3

CONFIRMATION NO. 3793

27975

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UPDATED FILING RECEIPT

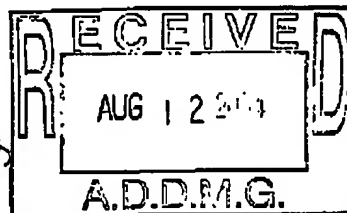


Date Mailed: 08/09/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

STMicroelectronics S.r.l., Agrate Brianza, ITALY; (ME)

Domestic Priority data as claimed by applicant

Foreign Applications

EUROPEAN PATENT OFFICE (EPO) 03425054.8 01/31/2003

If Required, Foreign Filing License Granted: 06/15/2004

Projected Publication Date: 11/18/2004

Non-Publication Request: No

Early Publication Request: No

Title

Reconfigurable signal processing IC with an embedded flash memory device

Preliminary Class

712

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Title 35, United States Code, Section 184
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